

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	MOS TRANSISTOR HAVING REDUCED SOURCE/DRAIN EXTENSION SHEET RESISTANCE		
Application Number :			
Date :			
First Named Applicant:		Chin-Cheng Chien	
Confirmation Number:			
Attorney Docket Number:		NAUP0534USA	
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Documents being submitted:	Files
us-fee-sheet	NAUP0534-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	NAUP0534-usdecl.xml us-declaration.dtd us-declaration.xsl
us-power-of-attorney-grant	NAUP0534-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-assignment	NAUP0534-usassn.xml us-assignment.xsl us-assignment.dtd NAUP0534ASS1.tif NAUP0534ASS2.tif
us-request	NAUP0534-usrequ.xml us-request.dtd us-request.xsl
application-body	NAUP0534-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Image2.tif naup0534usa-01.tif naup0534usa-02.tif naup0534usa-03.tif

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	naup0534usa-11.tif
	naup0534usa-12.tif
us-declaration	NAUP0534DEC1.tif
us-declaration	NAUP0534DEC2.tif
Comments	